

# Advanced Microprocessor Question Answer Diploma

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(2) Answer each next main Question on a new page (3) Illustrate your answers with neat sketches wherever necessary (4) Figures to the right indicate full marks (5) Assume suitable data, if necessary 1 (a) Attempt any FIVE of the following: [20] (i) State the features of 80386 microprocessor (ii) Differentiate between COM and EXE programs

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TY Diploma : Sem VI [CD/CO/CM] Advanced Microprocessor Time: 3 Hrs] Prelim Question Paper [Marks : 100 Instructions : (1) All questions are compulsory (2) Answer each next main Question on a new page (3) Illustrate your answers with neat sketches wherever necessary (4) Figures to the right indicate full marks

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#### ADVANCED MICROPROCESSOR Subject Title : Advanced ...

Diploma Course in Electronic and Communications Fifth Semester Subject: Advanced Microprocessors (Elective) MODEL QUESTION PAPER

Instructions 1) Section I is compulsory 2) Answer any 2 full questions from Each Section II, III, IV 3) Maximum Marks: 100 SECTION I 1 a) Fill in the blanks with appropriate word(s) (1\*5=5)

### Scanned by CamScanner

(Answer one full question from each unit Each full question carries 15 marks) 111 (a) (b) (b) (b) (b) VII (a) (b) WII (a) (b) rx (a) (b) UNIT With a neat diagram briefly explain the internal architecture of Intel 8086 Define parity flag and auxiliary flag registers in 8086 OR Explain physical address generation in 8086

### Microprocessor/Microcontroller - Electronics

Microprocessor/Microcontroller •The first microprocessor to make it into a home computer was the Intel 8080, a complete 8-bit computer on one chip,

### COLLEGE OF ENGINEERING DEPARTMENT OF COMPUTER ...

4 Microprocessor and Microcontroller CSE KCE PART-B 1 Describe Intel 8086 Microprocessor Architecture 2 Describe any five addressing modes of 8086 with suitable examples 3 Write a 8086 ALP to convert an 8 bit binary number into equivalent gray code

### MICROCONTROLLER QUESTION BANK UNIT-I PART-A

MICROCONTROLLER QUESTION BANK UNIT-I PART-A 1 What is microcontroller? 2 How many I/O ports are placed in microcontroller 8051? 3 Define DPTR 4 What is the purpose of PSW register? 5 What is use of EA pin? 6 How many bit addressable location are placed in internal RAM? 7 What is interrupts signal? 8 Name the types of 8051 interrupts

### Microprocessors - tutorialspoint.com

Microprocessors 6 Microprocessor is a controlling unit of a micro-computer, fabricated on a small chip capable of performing ALU (Arithmetic Logical Unit) operations and communicating with the other devices connected to it Microprocessor consists of an ALU, register array, and a control unit ALU performs

### Chapter One Introduction to Computer

Chapter One Introduction to Computer Computer A computer is an electronic device, operating under the control of instructions stored in its own memory that can accept data (input), process the data according to specified rules, produce information (output), and store the ...

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### DIPLOMA - VIEP - EIXOTRONICS AND Term-End Examination ...

Question no 1 is compulsory All questions carry equal marks Choose the correct answer from the given four alternatives 7x2=14 (a) Which feature of microcontroller is not available in a microprocessor based system (1) Registers (ii) Program memory lock (iii) ADC (iv) None of these (b) Reset input for 8051 (i) clears all ports (ii) loads SP

### Toyota Coaster Owners Manual - CTSNet

Market Insight Fifth 5th Edition The Naked Traveler 3 Trinity Pdf Zapallitojeldres Advanced Microprocessor Question Answer Diploma Sitemap Popular Random Top Powered by TCPDF (wwwtcpdforg) 2 / 2 Title: Toyota Coaster Owners Manual Author: Ursula Faber Subject: Toyota Coaster Owners Manual

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**CSE 30321 - Computer Architecture I - Fall 2010 Final Exam ...**

CSE 30321 - Computer Architecture I - Fall 2010 Final Exam December 13, 2010 Test Guidelines: 1 Place your name on EACH page of the test in the space provided 2 Answer every question in the space provided If separate sheets are needed, make sure to The average memory access time for a microprocessor with 1 level of cache is 24